**Dr. ANURADHA SAVADI**

Faculty of Enginerring & Technology (Co-Education), Department of Electronics and Communication Engineering Sharnbasva University, Kalaburagi, Karnataka, India

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**EDUCATION**

**Ph.D. :** Faculty of Electronics & Electrical Engineering Sciences, PDA College of Engineering Kalaburagi, Research Center, Visvesvaraya technological University, Belagavi

**M.Tech.**: VLSI DESIGN & Embedded Systems, VTU Extension Centre UTL Technologies, Bangaluru, Affiliated to Visvesvaraya technological University, Belagavi, 2007.

**B.E.:** Electronics & Communication Engineering, GND Engineering College, Bidar, Affiliated to Visvesvaraya technological University, Belagavi, 2004.

**PROFESSIONAL EXPERIENCE**

* October 2021 to till date Associate Professor, Sharnbasva University, Kalaburagi.
* 2019-Sept.2021 Assistant Professor, Sharnbasva University, Kalaburagi .
* 2012-2019 Assistant Professor, APPA Institute of Engineering & Technology, Kalaburagi
* 2010-2012 Lecturer, APPA Institute of Engineering & Technology, Kalaburagi.
* 2008-2009 Lecturer, YDIT Bengaluru.
* 2006-2008 Engineer, National Aerospace Laboratory(NAL), (R&D)
* 2004-2005 Lecturer, YDIT Bengaluru.

**ADMINISTRATIVE EXPERIENCE**

* Serving as Member Secretary, IIIT Cell, Sharnbasva University, Kalaburagi.
* Serving as BOE and BOS member of Electronics & Communication Engineering programme, Sharnbasva University, Kalaburagi.
* Served as PG coordinator in the department of Electronics and Communication Engineering.
* Served as Project coordinator in the department of Electronics & Communication Engineering.
* Served as NAAC Co-coordinator of Electronics & Communication Engineering programme, Sharnbasva University, Kalaburagi.
* Served as Ph.D coordinator in the department of Electronics and Communication Engineering.

**PUBLICATIONS**

[1] Anuradha Savadi, “DESIGN OF A 16 BIT ARITHMETIC AND LOGICAL UNIT USING VEDIC MULTIPLICATION TECHNQUE”, published in IJAER & presented in International Conference on Power, Circuit and Information (ICPCIT), 2015 International Conference on 27-28 Apr. 2015, pp 31479-31482, ISSN 0973-4562 Vol. 10 No.44 (2015). Sponsored by **Springer**, organized by RajRajeswari College of Engineering, Bangalore.

[2] Anuradha Savadi, DR. Raju Yanamshetti “DESIGN AND IMPLEMENTATION OF 64 BIT IIR FILTERS USING VEDIC MULTIPLIERS”, presented in Elsevier International Conference on Computational Modeling and Security (Elsevier CMS 2016). Sponsored by **Elsevier and Procedia computer science**, organized by RL Jalappa institute of technology, Banglore.

[3] Anuradha Savadi, DR. Raju Yanamshetti “A SURVEY ON DESIGN OF DIGITAL SIGNAL PROCESSOR”, published in proceedings of IEEE International Conference on Wireless Communications, Signal Processing and Networking (WiSPNET 2016) and IEEE Xplore, organized by SSN College of Engineering, Chennai.

[4] Anuradha Savadi, DR. Raju Yanamshetti “ Impact of priority based heading one detector on H.265/HEVC”, IOSR Journal of VLSI and signal processing. Vol. 8 Issue 2, ver. 1 mar-apr- 2018.

[5] Anuradha Savadi, DR. Raju Yanamshetti “DESIGN AND SYNTHESIS OF HIGH PERFORMANCE VEDIC DSP PROCESSOR” international journal of computer application, Vol 168, no.16 june 2017, pp np. 27-32

[6] Anuradha Savadi, DR. Raju Yanamshetti “Design of Nova decoder for H.265/HEVC”, International Journal Of Computer Application, Vol 179, no.11 jan 2018, pp no. 35-40

[7] Anuradha Savadi, DR. Raju Yanamshetti “Performance Boosting Components of Vedic DSP Processor”, International Journal on Recent and Innovation Trends in Computing and Communication, VOl -5 Issue 5, May 2017, pp. no 960-964.

[8] Anuradha Savadi, DR. Raju Yanamshetti “Retention of High Efficiency Video Coding (HEVC)”, international journal for advances research in applied science, Vol -5 , issue 3, March 2018. Pp.no 176-180.

[9] Anuradha Savadi, DR. Raju Yanamshetti “Design of Discrete Cosine Transform for H.265/HEVC using vedic Algorithms”, presented in International Conference on Recent Trends In Science Engineering, Humanities & Social Sciences 30th august 2019.

[10] Anuradha Savadi, Nivedita Sajjan “Serial Divider Design using LabView”, presented in National Conference on Engineering Business Management, Science, Humanities & Social Sciences-2022.

[11] Anuradha Savadi, Vasia Ibtasaum “Parallel Divider Design using LabView”, presented in National Conference on Engineering Business Management, Science, Humanities & Social Sciences-2022.

[12] Anuradha Savadi, Nivedita Sajjan “Password Based Security Lock System”, presented in National Conference on Engineering Business Management, Science, Humanities & Social Sciences-2022.

[13] Anuradha Savadi “Design And Implementation Of Point Processors Using Xilinx System Generator on FPGA”, presented in National Conference on Recent Innovations In Information, Communication Technology, And Management. Held at VTU PG centre and regional office kusnoor road Kalaburagi. 21-22 may 2015

[14] Anuradha Savadi “low power universal gate design using AVL Techniques in Mentor Graphics”, presented in National Conference on Recent Innovations In engineering and Technology 7th to 8th November 2015. Published in Indian journal of scientific research. vol.12 November 2015.

[15] Anuradha Savadi “low power multiplexer design using Mentor Graphics”, presented in National Conference on Recent Innovations In engineering and Technology 7th to 8th November 2015. Published in Indian journal of scientific research. vol.12 November 2015.

[16] Anuradha Savadi “Design Of High Speed Area Optimized Binary Coded Decimal Digit Adder”, presented in National Conference on Recent Innovations In engineering and Technology 3rd to 4th May 2014. Published in International Journal Of Research In Engineering And Technology.

**Training Programmes /Workshops/Seminars/STTPs/Symposiums/Webinars attended:**

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| Outcome based Education (OBE) | 11th Feb 2019 | 11th Feb 2019 | organized by Department of Electronics and Communication Engineering Faculty of Engineering and Technology(Co-Education) SUK in association with The institute of engineers ( INDIA) – IEI |
| Applications of LabVIEW In Teaching & Research | 17th June 2019 | 22nd June 2019 | organized by Department of Electronics and Communication Engineering Faculty of Engineering and Technology, SUK in association with Industry Institution Interaction Cell under IQAC. |
| One Week State Level Faculty Development Program on Emerging Trends in Teaching , Learning & Research | 4th January 2016 | 9th January 2016 | organized by APPA Institute of Engineering & Technology, Kalaburagi and Godutai Engineering College for Women Kalaburagi in association with VNEC, Shorapur & LAEC, Bidar |
| Effective Teaching , Learning & Research Skills Towards Global Standards | 19th July 2016 | 24th July 2016 | organized by APPA Institute of Engineering & Technology, Kalaburagi and Godutai Engineering College for Women Kalaburagi and Sharanbasaveshwara Science College PG Course, Kalaburagi. in association with VNEC, Shorapur & LAEC, Bidar |
| Recent Treand in VLSI Design and Embedded Systems | 5th May 2014 | 8th May 2014 | Organized by Department of Electronics and Communication Engineering , APPA Institute of Engineering & Technology, Kalaburagi. |
| Image Processing & Communication Systems (IPCS-2013) | 21st sept 2013 | 22nd sept 2013 | Organized by Department of Electronics and Communication Engineering , APPA Institute of Engineering & Technology, Kalaburagi. in association with IETE new Delhi & iste Bengaluru Chapter. |
| Emerging & Transformative Technologies Post Pandemic Era | 29th June 2020 | 1st July 2020 | Organized by Department of Electronics and Communication Engineering Faculty of Engineering and Technology(Co-Education) SUK Kalaburagi |
| Hands on training on LaTex for Technical Document preparation | 18th January 2022 | 19th January 2022 | Organized by Department of Electronics and Communication Engineering Faculty of Engineering and Technology(Co-Education) SUK Kalaburagi |
| Deep learning based video analytics for surveillance IOI applications | 13th July 2020 | - | Organized by GSSSIETW IEEE student Branch in Association with IEEE Bangalore Section and CAS Bangalore chapter |
| Machine Learning with python and cloud deployment | 9th July 2020 | - | Organized by Department of CSE, Faculty of Engineering and Technology(Co-Education) SUK Kalaburagi |
| Workshop on Research Methodology | 23rd August 2014 | 23rd August 2014 | Organized by APPA Institute of Engineering & Technology, Kalaburagi. |
| Workshop on Embedded System Design Using Microcontrollers | 26th November 2007 | 1st December 2007 | Organized by VTU Belgaum |
| KSTA regional Conference on science and technology for development | 30th January 2014 | 31th January 2014 | Organized by school of earth sciences, central university of Karnataka, Gulbarga. |
| Lecture series in Analog IC Design | 29th June 2009 | 10th July 2009 | Conducted at Bhoomaraddi College of Engineering and Technology Hubli |
| Workshop on ARM9 | 14th November 2009 | - | Organized by ISM. |

**Patents:**

* A Self Adjusting Computer Webcam (2023).
* A Nanotechnology Based System For Boosting The Fertility Using IoNT (2021)

**Details of Memberships in Professional Societies:**

* ISTE Member.
* IETE Member.
* IEEE Senior Member.